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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Atsushi SANO et al.

Group Art Unit: 2811

Application No.: 10/574,571

Docket No.: 127570

Filed: April 28, 2006

For: MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND
SUBSTRATE PROCESSING APPARATUS

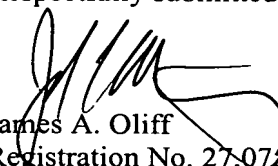
REQUEST FOR CORRECTION OF PALM RECORDS

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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,


James A. Oliff
Registration No. 27,073

Joel S. Armstrong
Registration No. 36,430

JAO:JSA/ejw

Date: December 20, 2006

OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

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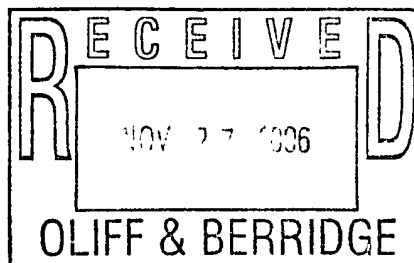
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/574,571	04/28/2006	2811	1150	127570	16	21	4

25944
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CONFIRMATION NO. 5708

FILING RECEIPT



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Date Mailed: 11/22/2006

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Applicant(s)

Atsushi Sano, Toyama, JAPAN;
Sadayoshi Horii, Toyama, JAPAN;
Hideharu Itatani, Toyama, JAPAN;
Masayuki Asai, Toyama, JAPAN;

Assignment For Published Patent Application

Hitachi ~~Kokusai~~ Electric Inc., Tokyo, JAPANHITACHI ~~KOKUSAI~~ ELECTRIC INC.Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP05/00751 01/21/2005

Foreign Applications

JAPAN 2004-012885 01/21/2004

If Required, Foreign Filing License Granted: 11/20/2006

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/574,571**

Projected Publication Date: 03/01/2007

Non-Publication Request: No

Early Publication Request: No

Title

Manufacturing method of semiconductor device and substrate processing apparatus

Preliminary Class

257

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